

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A field effect transistor mixer comprising:
 - ~~a)~~a- a balun that includes a transformer having a primary winding and a secondary winding, the primary winding coupled to a radio frequency signal input;
 - ~~b)~~b- a pair of field effect transistors, each transistor having a gate, a source, a drain, and a channel between the source and the drain, wherein
 - ~~i)~~i- the gates of the transistors are coupled to one another and to a local oscillator input,
 - ~~ii)~~ii- one of the source and the drain of a first of the two transistors is coupled at a node to one of the source and the drain of the other of the two transistors, and the node is coupled to ground,
 - ~~iii)~~iii- the other of the source and the drain of the first of the two transistors is coupled to one side of the secondary winding of the balun and the other of the source and the drain of the second of the two transistors is coupled to the other side of the secondary winding of the balun; and
 - ~~c)~~e- ~~and~~ an intermediate frequency signal output coupled to a point in the circuit path between the first and second transistors.
2. (Original) A mixer according to claim 1, wherein, at the node, one of the source and the drain of the second of the two transistors is connected to ground, and the node is coupled to one of the source and drain of the first of the two transistors by a filter.
3. (Currently Amended) A mixer according to claim 2, the filter having a capacitor serving as a shunt at the frequency of the ~~r~~Radio frequency signal but not at the frequency of the intermediate frequency signal.

4. (Original) A mixer according to claim 1, wherein the local oscillator input is coupled to the gates via a capacitor so as to cause the transistors to be biased near pinch off.
5. (Previously Presented) A mixer according to claim 1, further comprising a capacitance disposed across the balun secondary winding to tune the secondary winding.
6. (Original) A mixer according to claim 5, wherein the capacitance includes a pair of capacitors connected in series, wherein the node at a connection between the capacitors is coupled to ground.
7. (Previously Presented) A mixer according to claim 1, wherein the secondary winding has a center tap, and the center tap is coupled to ground.
8. (Previously Presented) A method of mixing a periodic signal and a radio frequency signal producing an intermediate frequency signal, the method comprising:
applying the radio frequency signal to a primary winding of a transformer of a balun;
applying a periodic signal simultaneously to a first and a second switch, each switch coupled to the other switch and also coupled to a secondary winding of the transformer of the balun, resulting in a frequency translation of the periodic signal and the radio frequency signal producing a mixed output; and
outputting the intermediate frequency signal by filtering the mixed output between the first and second switch.
9. (Original) A method according to claim 8, wherein the step of outputting the intermediate signal by filtering the output between the first and second switch includes coupling one switch at a node to ground, and coupling the other switch to the node by a filter.

10. (Currently Amended) A method according to claim 8, wherein the step of filtering of the mixed output includes applying a capacitance that serves as a shunt at the frequency of the ~~r~~Radio frequency signal but not at the frequency of the intermediate signal.

11. (Previously Presented) A method according to claim 8, further comprising applying a capacitance across the balun primary winding to tune the primary winding.

12. (Previously Presented) A method according to claim 8, further comprising applying a capacitance across the balun secondary winding to tune the secondary winding.

13. (Original) A method according to claim 12, wherein the capacitance includes a pair of capacitors in series, wherein the node at a connection between the capacitors is coupled to ground.

14. (Previously Presented) A method according to claim 8, further comprising coupling ground to the center tap of the balun secondary winding.

15. (Currently Amended) A field effect transistor ~~m~~Mixer comprising:

~~a)~~ a transformer having a primary winding and a secondary winding, the primary winding coupled to a ~~r~~Radio frequency signal;

~~b)~~ a pair of switches, comprising of a first switch and a second switch, wherein

~~i)~~ the gates of the switches are coupled to one another and to a periodic signal input,

~~ii)~~ the first switch is coupled at a node to ground, and the node is coupled to the second switch,

~~iii)~~ one switch is attached to one side of the secondary winding of the transformer, and the other switch is attached to the other side of the secondary winding of the transformer; and

c)e- an intermediate frequency signal output coupled to a point in the circuit path between the first and second transistors.

16. (Original) A mixer according to claim 15, wherein, at the node, one of the switches is connected to ground, and the node is coupled to the other switch by a filter.

17. (Currently Amended) A mixer according to claim 16, the filter having a capacitor serving as a shunt at the frequency of the ~~r~~Radio frequency signal but not at the frequency of the intermediate frequency signal.

18. (Previously Presented) A mixer according to claim 15, further comprising tuning circuitry disposed across the transformer's secondary winding.

19. (Previously Presented) A mixer according to claim 15, wherein the secondary winding of the transformer has a center tap, and the center tap is coupled to ground.

20. (Previously Presented) A mixer according to claim 15, further comprising tuning circuitry disposed across the primary winding of the transformer.

Amendments to the Drawings:

The attached sheets 1-6 of formal drawings include Figs. 1-8. The drawings have been amended to comply with various objections made by the Draftsperson under 37 CFR 1.84. These amendments include: amending Figs. 1-5 so as to remove erasures, alterations, overwriting, interlineations, folds, and copy machine marks; amending Figs. 1-5 and 8 to clearly define and make legible line numbers and letters; and amending the top margin of Fig. 5.

Additionally, the following reference numbers have been deleted: on figure 3, reference numbers A, B, E, G, 320, 348, 350, 370, 372, 374 and 392; on figure 4, reference numbers 1, 402, 404, 405, 407-411, 413, 420, 423 and 424; and on figure 8, reference number 802. The following reference numbers have been added to the drawings: in Fig. 1, transistor 101; and in Fig. 2, transistors 201 and 201. Fig. 1 has been labeled as --Prior Art--.

Attachments: Replacement Sheets 1-6 replacing Sheets 1-7. Replacement Sheets 1-6 include:

- Replacement Sheet 1 showing Figs. 1 and 2;
- Replacement Sheet 2 showing Fig. 3;
- Replacement Sheet 3 showing Fig. 4;
- Replacement Sheet 4 showing Fig. 5;
- Replacement Sheet 5 showing Figs. 6 and 7; and
- Replacement Sheet 6 showing Fig. 8.